

Day : Wednesday

Date: 1/31/2007
Time: 10:06:50 **PALM INTRANET**

Inventor Information for 10/770407

Inventor Name	City	State/Country
MAGRATH, ANTHONY J.	EDINBURGH	UNITED KINGDOM

Appln Info	Contents	Petition Info	Atty/Agent Info	Continuity/Reexam	Foreign I
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Last Name = MAGRATH

First Name = ANTHONY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10214641	Not Issued	61	08/09/2002	Bass compressor	MAGRATH, ANTHONY J.
10770407	Not Issued	30	02/04/2004	Class D amplifier	MAGRATH, ANTHONY J.
10777763	7061415	150	02/13/2004	WORD LENGTH REDUCTION CIRCUIT	MAGRATH, ANTHONY J.
10828342	Not Issued	30	04/21/2004	Signal processors and associated methods	MAGRATH, ANTHONY J.
11002207	Not Issued	30	12/03/2004	Audio processing	MAGRATH, ANTHONY J.
11110858	Not Issued	93	04/21/2005	PULSE WIDTH MODULATOR QUANTISATION CIRCUIT	MAGRATH, ANTHONY J.
11225845	Not Issued	30	09/14/2005	Feedback controller for PWM amplifier	MAGRATH, ANTHONY JAMES

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<in>metadata)
- #2 (((bit* <or> symbol* <or> pulse*) <near/4> (invert* or flipp*))
<and> (delta <near/3/> sigma))<in>metadata)
- #3 (((bit* <or> symbol* <or> pulse*) <near/4> (invert* or flipp*))
<and> (delta <near/3/> sigma))<in>metadata)
- #4 (((bit* <or> symbol* <or> pulse*) <near/4> (invert* or flipp*))
<and> (delta <near/3/> sigma))<in>metadata)
- #5 (((bit* <or> symbol* <or> pulse*) <near/4> (invert* or flipp*))
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- #6 (((bit* <or> symbol* <or> pulse*) <near/4> (invert* or flipp*))
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- #8 (((bit* <or> symbol* <or> pulse*) <near/4> (invert* or flipp*))
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- #9 (((bit* <or> symbol* <or> pulse*) <near/4> (invert* or flipp*))
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- #10 (((bit* <or> symbol* <or> pulse*) <near/4> (invert* or flipp*))
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- #11 (((bit* <or> symbol* <or> pulse*) <near/4> (invert* or flipp*))
<and> (delta <near/3/> sigma))<in>metadata)
- #12 (((look <near/2> (up <or> ahead)) <near/3> quantiz*) <and>
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IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Delta modulation strategies for resonant link inverters**
Kheraluwala, M.H.; Divan, D.M.;
[Power Electronics, IEEE Transactions on](#)
Volume 5, Issue 2, April 1990 Page(s):220 - 228
Digital Object Identifier 10.1109/63.53159
[AbstractPlus](#) | Full Text: [PDF](#)(608 KB) IEEE JNL
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- ☐ 2. **Resolution enhancement and dither of sigma-delta modulator digital-to-a converters**
Magrath, A.J.; Sandler, M.B.;
[Electronics Letters](#)
Volume 31, Issue 18, 31 Aug. 1995 Page(s):1540 - 1542
[AbstractPlus](#) | Full Text: [PDF](#)(240 KB) IEE JNL
- ☐ 3. **Efficient dithering of sigma-delta modulators with adaptive bit flipping**
Magrath, A.J.; Sandler, M.B.;
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Volume 31, Issue 11, 25 May 1995 Page(s):846 - 847
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- ☐ 4. **Thinned-out controlled class D inverter with delta-sigma modulated 1-bit**
Koizumi, H.; Kurokawa, K.; Mori, S.;
[Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on](#)
23-26 May 2005 Page(s):1322 - 1325 Vol. 2
Digital Object Identifier 10.1109/ISCAS.2005.1464839
[AbstractPlus](#) | Full Text: [PDF](#)(456 KB) IEEE CNF
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- ☐ 5. **Amplitude bounds on oscillations from a sigma-delta modulator structure**
Davies, A.C.;
[Circuits and Systems, 1998. ISCAS '98. Proceedings of the 1998 IEEE Interna](#)
[on](#)
Volume 3, 31 May-3 June 1998 Page(s):298 - 301 vol.3
Digital Object Identifier 10.1109/ISCAS.1998.704009
[AbstractPlus](#) | Full Text: [PDF](#)(276 KB) IEEE CNF
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- ☐ 6. **A sigma-delta modulator topology with high linearity**
Magrath, A.J.; Sandler, M.B.;
Circuits and Systems, 1997. ISCAS '97. Proceedings of 1997 IEEE International
Volume 1, 9-12 June 1997 Page(s):53 - 56 vol.1
Digital Object Identifier 10.1109/ISCAS.1997.608521
[AbstractPlus](#) | Full Text: [PDF](#)(344 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 7. **Design and implementation of a FPGA sigma-delta power DAC**
Magrath, A.J.; Clark, I.G.; Sandler, M.B.;
Signal Processing Systems, 1997. SIPS 97 - Design and Implementation., 199
on
3-5 Nov. 1997 Page(s):511 - 521
Digital Object Identifier 10.1109/SIPS.1997.626339
[AbstractPlus](#) | Full Text: [PDF](#)(492 KB) IEEE CNF
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- ☐ 8. **High-gain feedback in a delta-sigma modulated inverter with carrier**
Inoue, T.; Nishimura, K.; Gamou, S.; Kashiwagi, H.;
Industrial Electronics, Control and Instrumentation, 1991. Proceedings. IECON
International Conference on
28 Oct.-1 Nov. 1991 Page(s):191 - 196 vol.1
Digital Object Identifier 10.1109/IECON.1991.239307
[AbstractPlus](#) | Full Text: [PDF](#)(432 KB) IEEE CNF
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- ☐ 9. **Performance analysis of three-phase inverters controlled by synchronous modulation systems**
Mertens, A.;
Industry Applications Society Annual Meeting, 1992., Conference Record of the
4-9 Oct. 1992 Page(s):779 - 788 vol.1
Digital Object Identifier 10.1109/IAS.1992.244317
[AbstractPlus](#) | Full Text: [PDF](#)(648 KB) IEEE CNF
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- ☐ 10. **Non-linear deterministic dithering of sigma-delta modulators**
Magrath, A.J.; Sandler, M.B.;
Oversampling and Sigma-Delta Strategies for DSP, IEE Colloquium on
23 Nov 1995 Page(s):2/1 - 2/6
[AbstractPlus](#) | Full Text: [PDF](#)(384 KB) IEEE CNF

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